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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/091,738	03/06/2002	Stewart R. Wyatt	10018462-1	8159

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HEWLETT-PACKARD COMPANY  
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P.O. Box 272400  
Fort Collins, CO 80527-2400

EXAMINER

LAMARRE, GUY J

ART UNIT	PAPER NUMBER
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2133

DATE MAILED: 12/09/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

10/091,738

Applicant(s)

WYATT ET AL.

Examiner

Guy J. Lamarre, P.E.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 06 March 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 03/06/02 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

### DETAILED ACTION

0. Pursuant to 35 USC 131, **Claims 1-20** are presented for examination.

#### Claim Rejections - 35 USC § 101

1. 35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

- 1.1 **Claims 16-17** are rejected under 35 U.S.C. 101 as claiming non-statutory subject matter: a program. Applicant is advised to modify limitations/preamble of said claims with computer generating/execution means.

#### Specification

2. Updated serial/patent numbers are to be provided in the disclosure, e.g., on the 6<sup>th</sup> page.
- 2.1 The specification has not been checked to the extent necessary to determine the presence of all possible minor errors, e.g., “one parity symbol” instead of ‘one symbol’ on line 19 of page
3. Applicant's cooperation is requested in correcting any errors which applicant may become aware of in the specification.

Appropriate correction is required.

#### Claim Rejections - 35 USC § 112

3. The following is a quotation of the **second** paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

- 3.1 **Claim 13 is** rejected under the second paragraph of 35 U.S.C. 112 because it is not clear to the **Examiner** what is meant by: ‘a data path...’ on last line.

#### Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

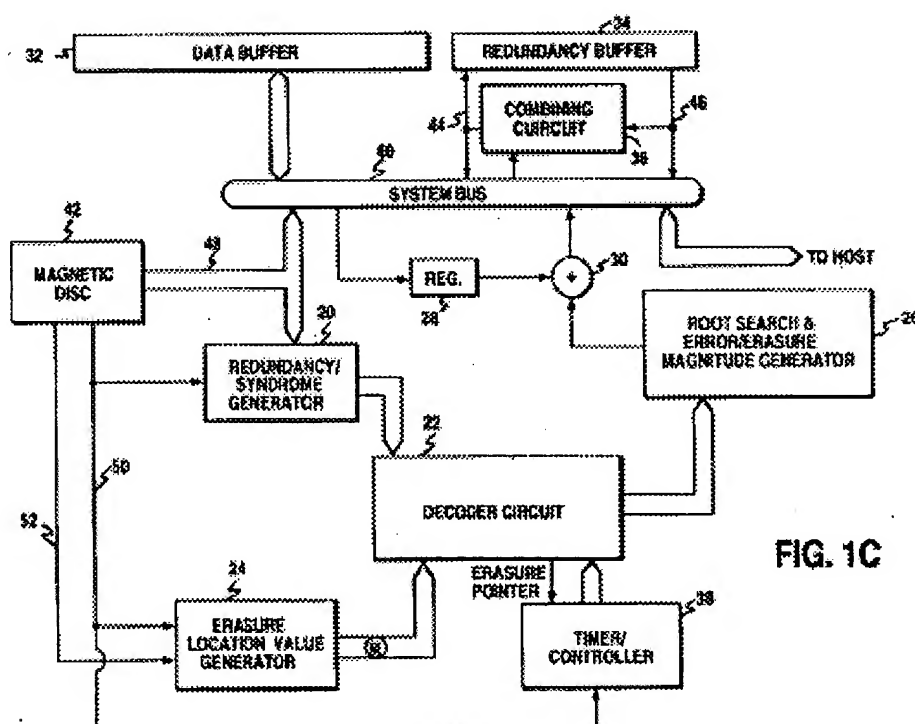
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A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4.1 Claims 1-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Glover (US Patent No. 5,751,733; 12 May 1998).

As per Claims 1-20, Glover discloses an equivalent data transfer pausing procedure and configuration in "Interleaved redundancy sector for correcting an unrecoverable sector in a disc storage device," as depicted in Fig. 1c and described in col. 6 line 10 et seq., wherein, when an error threshold is reached, a 'storage system pauses the data transfer and executes the track level error correction steps to recover the lost sector using the redundancy sector...' along with the use of erasure pointers.



Glover discloses the claimed system for pausing data transfer e.g., in col. 7 line 24 et seq and the verify command comprising: full error/erasure tracking means (col. 4 line 2 and Fig. 1c: Numerals 20, 24, 26), sparing means in Fig. 4c: Numeral 216), error condition threshold

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setting means along with compare means to evaluate match or mismatch with ECC error capability, such as ECC error capability comprising means to detect/correct full errors and erasures, e.g., in col. 7 line 22 et seq., data recovery at e.g., in Figs. 3c, 4b, 6a-6c and col. 7 lines 28, 30 et seq., write verify operation/command means in Figs. 4e-4f, 6a-6c, e.g., "As mentioned above, there are two situations where a data sector on the disc may become unrecoverable. First, the sector may become entirely unreadable due to an inability to synchronize to the sector data (because, for example, the preamble 6 or sync mark 8 have been corrupted by a defect on the medium). The other possibility is that the sector becomes uncorrectable; that is, the number of hard errors exceeds the error correction capability of the sector level ECS. In these situations, the storage system pauses the data transfer and executes the track level error correction steps to recover the lost sector using the redundancy sector.

...The prior art track level error correction systems are limited to correcting only one unrecoverable data sector per track because the redundancy sector is generated as the byte XOR of the respective data bytes in the data sectors. This severely limits the benefit of using a redundancy sector, especially in cases where a burst error spans two sectors, thereby rendering both sectors unrecoverable at the sector level and at the track level. The present invention improves the error correction capability of the track level ECS by dividing a sector into three interleaved codewords and generating the redundancy sector by combining the respective symbols in each codeword according to a predetermined error correction operation (e.g., byte XOR)..... This aspect of the present invention is understood with reference to FIG. 9 which shows each data sector divided into three codewords, and the codewords being combined (XORed) across three interleaves (designated INTLV 0, INTLV 1 and INTLV 2) to generate an interleaved redundancy sector. The data sector itself is interleaved to generate the three codewords; that is, symbol 0 is placed in the first codeword, symbol 1 is placed in the second codeword, symbol 2 is placed in the third codeword, symbol 3 is placed in the first codeword, etc.. Then, sector level redundancy is generated for each of the three codewords and stored in the data sector. Upon read back, the data symbols read from the disc are

*de-interleaved into the three codewords and each codeword is processed by the sector level ECS separately. In this manner, the sector level ECS can generate an erasure pointer corresponding to an unrecoverable codeword within a sector (i.e., an unrecoverable codeword in INTLV0, INTLV1 or INTLV2). Using the erasure pointers, the track level ECS is capable of correcting a single unrecoverable codeword in each interleave, and the unrecoverable codewords can occur in different sectors. Thus, using the interleave technique of the present invention, the track level ECS is capable of correcting up to three unrecoverable data sectors containing a single uncorrectable codeword in separate interleaves. ... Preferably, the redundancy sector is generated according to:  $2^{\text{sup}.m}$  --the sum modulo  $2^{\text{sup}.m}$  of the respective codeword symbols in an interleave (i.e., INTLV0, INTLV1 or INTLV2), where  $m$  is the size in bits of a codeword symbol. Then, the track level error syndromes for correcting a codeword are generated as the sum modulo  $2^{\text{sup}.m}$  of the respective codeword symbols in an interleave, including the redundancy sector codeword. The error syndromes are then used to correct a data codeword uncorrectable at the sector level that corresponds to the erasure pointer generated by the sector level ECS. That is, the erasure pointer identifies the sector and interleave location of the uncorrectable codeword, and the track level ECS uses the erasure pointers to correct up to three codewords in separate interleaves which can occur in three different sectors.'*

### **Conclusion**

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

5.1 Any response to this action should be mailed to:

Commissioner of Patents and Trademarks, Washington, D.C. 20231

**or faxed to:** (703) 872-9306 for all formal communications.

Hand-delivered responses should be brought to Customer Services, 220 20<sup>th</sup> Street S., Crystal Plaza II, Lobby, Room 1B03, Arlington, VA 22202.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Guy J. Lamarre, P.E., whose telephone number is (571) 272-3826. The examiner can normally be reached on Monday to Friday from 9:30 AM to 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert De Cady, can be reached at (571) 272-3819.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571) 272-3609.

Information regarding the status of an application may also be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Guy J. Lamarre, P.E.  
Primary Examiner  
12/2/04

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